



Date: 19-10-2020

DEPARTMENT OF CIVIL ENGINEERING

Revised External Lab (Regular/Supple) Schedule Oct - 2020

III B. Tech (CIVIL) II Sem – R16

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
21-10-2020 (Wednesday)	Geotechnical Engineering Lab– R1632016	A	B	C
	Environmental Engineering Lab – R1632017	D	E	F
22-10-2020 (Thursday)	Environmental Engineering Lab – R1632017	A	B	C
	Computer Aided Engineering Lab – R1632018	D	E	F
23-10-2020 (Friday)	Computer Aided Engineering Lab – R1632018	A	B	C
	Geotechnical Engineering Lab– R1632016	D	E	F

Batch Members (93)

A: 17H71A0101 – 17H71A0122, 16H71A0173	(16)
B: 17H71A0123 – 17H71A0143	(16)
C: 17H71A0144 – 17H71A0161	(16)
D: 17H71A0163 – 17H71A0182	(16)
E: 17H71A0184 -190,17H75A0101, 18H75A0101 -112	(16)
F: 18H75A0113 – 18H75A0125	(13)


Chief Superintendent

Chief Superintendent of the Examinations
Devineni Venkataramana & Dr. Hima Sekhar
MIC College of Technology
WANCHIKACHERI



Date: 16-10-2020

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

External Lab (Regular/Supple) Schedule Oct - 2020

III B. Tech (EEE) II Sem – R16

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
21-10-2020 (Wednesday)	Power Electronics Lab – R1632026	A	B	C
	Microprocessors & Microcontrollers Lab – R1632027	D	E	F
22-10-2020 (Thursday)	Data Structures Lab – R1632028	A	B	C
	Power Electronics Lab – R1632026	D	E	F
23-10-2020 (Friday)	Microprocessors & Microcontrollers Lab – R1632027	A	B	C
	Data Structures Lab – R1632028	D	E	F

Batch Members (114)

- A: 15H71A0277, 17H71A0201 – 17H71A0221 (20)
B: 17H71A0222 – 17H71A0241 (20)
C: 17H71A0243 – 17H71A0265 (20)
D: 17H71A0266, 267, 17H75A0219
18H75A0201 – 18H75A0218 (20)
E: 18H75A0219 – 18H75A0240 (20)
F: 18H75A0241 – 18H75A0255 & All Supple Candidates (14)


Chief Superintendent
Chief Superintendent of the Examination
Devineni Venkataramana & Dr. Hima Sekhar
MIC College of Technology
RANCHIKACHERLA



Date: 16-10-2020

DEPARTMENT OF MECHANICAL ENGINEERING


External Lab (Regular/Supple) Schedule Oct - 2020

III B. Tech (MECH) II Sem – R16

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
21-10-2020 (Wednesday)	Heat Transfer Lab – R1632036	A	B	C
	Metrology & Instrumentation Lab – R1632037	D	E	F
	Computational Fluid Dynamics Lab– R1632038	G	H	I
22-10-2020 (Thursday)	Heat Transfer Lab – R1632036	D	E	F
	Metrology & Instrumentation Lab – R1632037	G	H	I
	Computational Fluid Dynamics Lab– R1632038	A	B	C
23-10-2020 (Friday)	Heat Transfer Lab – R1632036	G	H	I
	Metrology & Instrumentation Lab – R1632037	A	B	C
	Computational Fluid Dynamics Lab– R1632038	D	E	F

Batch Members (161)

A: 16H71A0359, 366,398,3A6, 17H71A0301 – 314	(18)
B: 17H71A0315 – 17H71A0337	(18)
C: 17H71A0339 – 17H71A0358	(18)
D: 17H71A0361 – 17H71A0385	(18)
E: 17H71A0386 – 17H71A03A7	(18)
F: 17H71A03A9-3C0, 18H75A0301 –306	(18)
G: 18H75A0307 – 18 H75A0324	(18)
H: 18H75A0325 – 18 H75A0342	(18)
I: 18H75A0343 – 18 H75A0359	(17)


Chief Superintendent
Chief Superintendent of the Government
Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
HANCHERACHILLA



Date: 16-10-2020

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

External Lab (Regular/Supple) Schedule Oct - 2020

III B. Tech (ECE) II Sem – R16

Date	Name of the Lab	09:00–12:00	11:00–02:00	01:00–04:00
21-10-2020 (Wednesday)	Micro Processors & Micro Controllers Lab – R1632046	A	B	C
	VLSI Lab – R1632047	D	E	F
	Digital Communications Lab – R1632048	G	H	I
22-10-2020 (Thursday)	Micro Processors & Micro Controllers Lab – R1632046	D	E	F
	VLSI Lab – R1632047	G	H	I
	Digital Communications Lab – R1632048	A	B	C
23-10-2020 (Friday)	Micro Processors & Micro Controllers Lab – R1632046	G	H	I
	VLSI Lab – R1632047	A	B	C
	Digital Communications Lab – R1632048	D	E	F

Batch Members (195)

A: 17H71A0401 – 17H71A0421, 16H71A0433, 4B4	(22)
B: 17H71A0422 – 17H71A0444	(22)
C: 17H71A0445– 17H71A0467	(22)
D: 17H71A0468 – 17H71A0490	(22)
E: 17H71A0491 – 17H71A04B2	(22)
F: 17H71A04B3 – 17H71A04D4	(22)
G: 17H71A04D5 – 17H71A04F7	(22)
H: 17H71A04F8 – 17H71A04H8	(21)
I: 18H75A0401 – 18H75A0420 & All Supple Candidates	(20)

Chief Superintendent



Date: 16-10-2020

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

External Lab (Regular/Supple) Schedule Oct - 2020

III B. Tech (CSE) I Sem – R16

Date	Name of the Lab	09:00–12:00	01:00–04:00
21-10-2020 (Wednesday)	Network Programming Lab – R1632056	A	B
	Software Testing Lab – R1631057	C	D
	Data Warehousing and Mining Lab - R1632058	E	F
22-10-2020 (Thursday)	Network Programming Lab – R1632056	C	D
	Software Testing Lab – R1631057	E	F
	Data Warehousing and Mining Lab - R1632058	A	B
23-10-2020 (Friday)	Network Programming Lab – R1632056	E	F
	Software Testing Lab – R1631057	A	B
	Data Warehousing and Mining Lab - R1632058	C	D

Batch Members (161)

- A: 14H71A0542, 17H71A0502– 531 (27)
B: 17H71A0532 – 17H71A0559 (27)
C: 17H71A0560 – 17H71A0588 (27)
D: 17H71A0589 – 17H71A05B7 (27)
E: 17H71A05B8 – 17H71A05E7 (27)
F: 17H71A05E8 – 17H71A05H2, 18H75A0502-505 (26)

Chief Superintendent

Devineni Venkata Ramana & Dr. Hima Sekhar
MIC College of Technology
RANCHIKACHERLA



Date: 16-10-2020

DEPARTMENT OF INFORMATION TECHNOLOGY

External Lab (Regular) Schedule Oct - 2020

III B. Tech (IT) I Sem – R16

Date	Name of the Lab	09:00–12:00	01:00–04:00
21-10-2020 (Wednesday)	Web Technologies Lab – R1632124	A	B
22-10-2020 (Thursday)	Software Testing Lab – R1632125	A	B
23-10-2020 (Friday)	Data Mining Lab – R1632126	A	B

Batch Members (56)

A: 17H71A1201 – 17H71A1229 (28)

B: 17H71A1230 – 17H71A1260 (28)

Chief Superintendent

Chief Superintendent of the Examinations
Devineni Venkataramana & Dr. Hima Sekhar
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